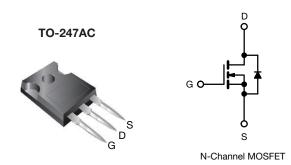


HALOGEN

FREE



EF Series Power MOSFET With Fast Body Diode



PRODUCT SUMMARY				
V _{DS} (V) at T _J max.	650			
R _{DS(on)} typ. (Ω) at 25 °C	V _{GS} = 10 V	0.036		
Q _g max. (nC)	126			
Q _{gs} (nC)	36			
Q _{gd} (nC)	26			
Configuration	Single			

FEATURES

- 4th generation E series technology
- Low figure-of-merit (FOM) Ron x Qg
- Low effective capacitance (Co(er))
- Reduced switching and conduction losses
- Avalanche energy rated (UIS)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting
- Industrial
 - Welding
 - Induction heating
 - Motor drives
 - Battery chargers
 - Renewable energy
 - Solar (PV inverters)

ORDERING INFORMATION			
Package	TO-247AC		
Lead (Pb)-free and halogen-free	SiHG039N60EF-GE3		

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-source voltage			V_{DS}	600	V	
Gate-source voltage			V_{GS}	± 30	V	
Continuous drain current (T _J = 150 °C)	V _{GS} at 10 V	T _C = 25 °C	- I _D	61	А	
		T _C = 100 °C		39		
Pulsed drain current ^a			I _{DM}	196		
Linear derating factor				2.9	W/°C	
Single pulse avalanche energy b			E _{AS}	508	mJ	
Maximum power dissipation			P_{D}	357	W	
Operating junction and storage temperature range			T _J , T _{stg}	-55 to +150	°C	
Drain-source voltage slope	T _J = 125 °C		dv/dt	70	V/ns	
Reverse diode dv/dt ^d			αν/αι	50	V/115	
Soldering recommendations (peak temperature) c	For 10 s			260	°C	

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature
- b. V_{DD} = 120 V, starting T_J = 25 °C, L = 28.2 mH, R_q = 25 Ω , I_{AS} = 6.0 A
- c. 1.6 mm from case
- d. $I_{SD} \le I_D$, di/dt = 160 A/ μ s, starting T_J = 25 °C



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THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Maximum junction-to-ambient	R_{thJA}	-	40	°C/W	
Maximum junction-to-case (drain)	R_{thJC}	-	0.35	C/VV	

PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT	
Static							•
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		600	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference to 25 °C, I _D = 10 mA		-	0.62	-	V/°C
Gate-source threshold voltage (N)	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		3.0	-	5.0	V
Out and the land		V _{GS} = ± 20 V		-	-	± 100	nA
Gate-source leakage	IGSS	$V_{GS} = \pm 30 \text{ V}$		-	-	± 1	μΑ
Zava gota valtaga dvain avvvant		V _{DS} =	480 V, V _{GS} = 0 V	-	-	1	μΑ
Zero gate voltage drain current	I _{DSS}	V _{DS} = 480 V	, V _{GS} = 0 V, T _J = 125 °C	-	-	2	mA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 32 A	-	0.036	0.040	Ω
Forward transconductance ^a	9 _{fs}	V _{DS} = 20 V, I _D = 32 A		-	18	-	S
Dynamic							•
Input capacitance	C _{iss}	$V_{GS} = 0 \text{ V},$ $V_{DS} = 100 \text{ V},$ f = 1 MHz		-	4323		pF
Output capacitance	C _{oss}			-	173	-	
Reverse transfer capacitance	C _{rss}			-	7	-	
Effective output capacitance, energy related ^a	C _{o(er)}	V _{DS} = 0 V to 480 V, V _{GS} = 0 V		-	139	-	
Effective output capacitance, time related ^b	C _{o(tr)}			-	858	-	
Total gate charge	Qg			-	84	126	
Gate-source charge	Q _{gs}	V _{GS} = 10 V	V _{GS} = 10 V I _D = 32 A, V _{DS} = 480 V		36	-	nC
Gate-drain charge	Q _{gd}			-	26	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 480 V, I _D = 32 A,		-	109	164	- ns
Rise time	t _r			-	172	258	
Turn-off delay time	t _{d(off)}		$V_{GS} = 10 \text{ V}, R_g = 24 \Omega$		152	228	
Fall time	t _f	1		-	78	117	
Gate input resistance	R _g	f = 1 MHz, open drain		0.2	0.7	1.4	Ω
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	61	
Pulsed diode forward current	I _{SM}			-	-	196	- A
Diode forward voltage	V _{SD}	T _J = 25 °C, I _S = 32 A, V _{GS} = 0 V		-	-	1.2	V
Reverse recovery time	t _{rr}	T _J = 25 °C, I _F = I _S = 32 A, di/dt = 100 A/ μ s, V _R = 400 V		-	169	338	ns
Reverse recovery charge	Q _{rr}			-	1.3	2.7	μC
Reverse recovery current	I _{RRM}			_	14.4	-	A

Notes

- a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS}



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

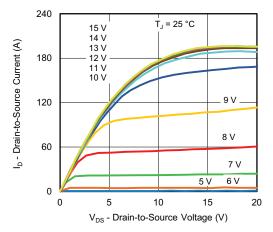


Fig. 1 - Typical Output Characteristics

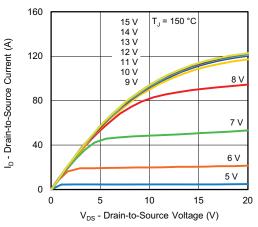


Fig. 2 - Typical Output Characteristics

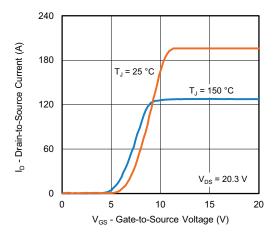


Fig. 3 - Typical Transfer Characteristics

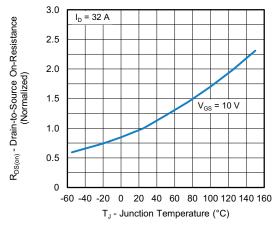


Fig. 4 - Normalized On-Resistance vs. Temperature

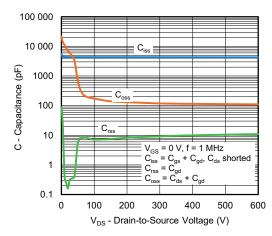


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

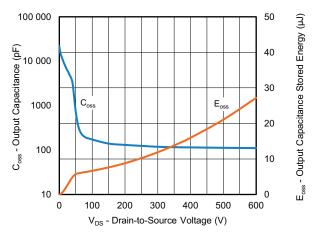


Fig. 6 - Coss and Eoss vs. VDS



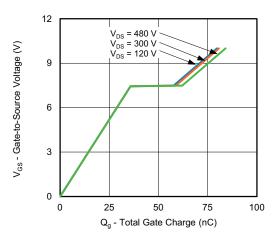


Fig. 7 - Typical Gate Charge vs. Gate-to-Source Voltage

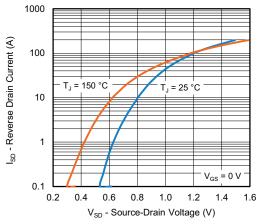


Fig. 8 - Typical Source-Drain Diode Forward Voltage

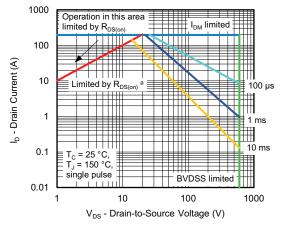


Fig. 9 - Maximum Safe Operating Area



a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

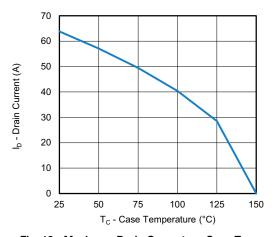


Fig. 10 - Maximum Drain Current vs. Case Temperature

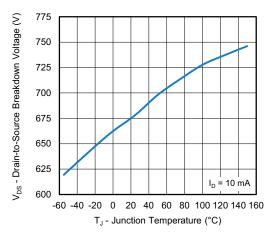


Fig. 11 - Temperature vs. Drain-to-Source Voltage



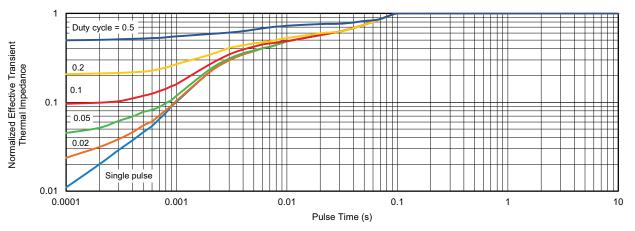


Fig. 12 - Normalized Transient Thermal Impedance, Junction-to-Case

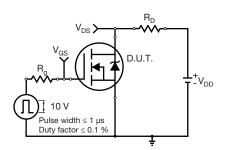


Fig. 13 - Switching Time Test Circuit

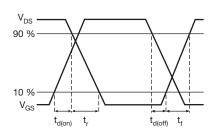


Fig. 14 - Switching Time Waveforms

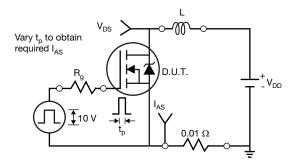


Fig. 15 - Unclamped Inductive Test Circuit

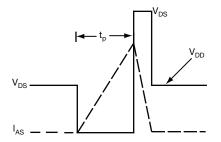


Fig. 16 - Unclamped Inductive Waveforms

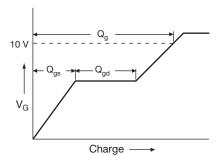


Fig. 17 - Basic Gate Charge Waveform

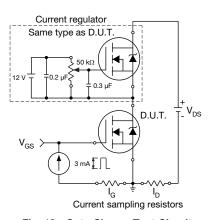
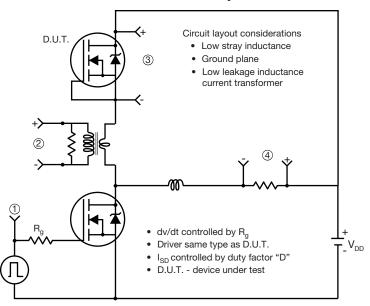


Fig. 18 - Gate Charge Test Circuit



Peak Diode Recovery dv/dt Test Circuit



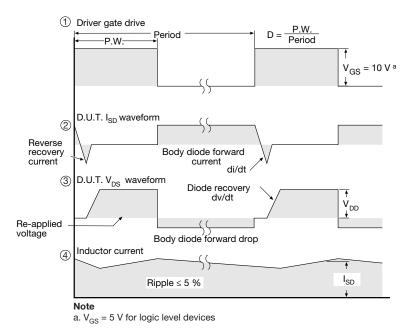


Fig. 19 - For N-Channel

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